



2128
JW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named
Inventor : Partha P. Datta Ray et al.

Appln. No.: 09/964,030

Filed : September 26, 2001

For : METHOD AND APPARATUS FOR
ADAPTIVE TIMING OPTIMIZATION
OF AN INTEGRATED CIRCUIT
DESIGN

Docket No.: 99-369/L13.12-0117

Group Art Unit: 2128

Examiner: Thai Q.
Phan

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I HEREBY CERTIFY THAT THIS PAPER IS BEING
SENT BY U.S. MAIL, FIRST CLASS, TO THE
COMMISSIONER FOR PATENTS, P.O. BOX 1450,
ALEXANDRIA, VA 22313-1450, THIS

12 DAY OF April, 2005

D. B.

PATENT ATTORNEY

Sir:

This is in response to the Office Action dated January
13, 2005. Please amend the above-identified application as
follows.